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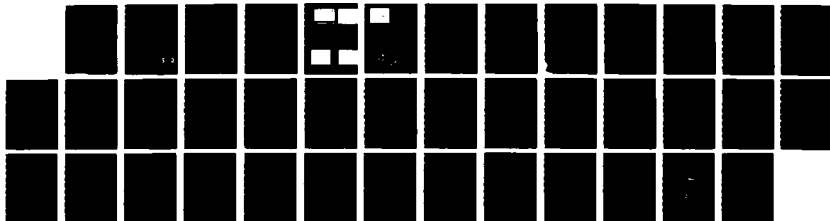
MILLIMETER-VISIBLE INJECTION LOCKING AND TESTING(U)
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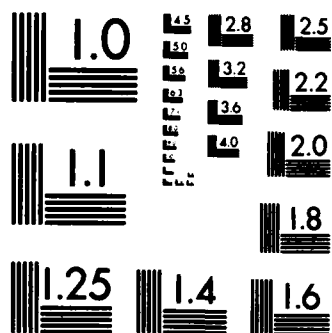
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Millimeter-Visible Injection Locking and Testing

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Abstract

A Visible-Millimeter Wave mixing system for testing high frequency devices has been set up with all components operating satisfactorily and locked to stabilized cavities. Using this system, mixing has been obtained, with frequency separations ranging to 100 GHz, in a number of GaAs and GaAs/AlGaAs devices. These devices include commercial FETs, state of the art industrial FETs as well as modulation doped HEMT structures and Heterojunction bi-polar transistors.

Research Summary

It has been extremely difficult to make frequency response measurements of devices at millimeter wave frequencies. We have therefore implemented several optical techniques to identify and measure the millimeter wave performance of our newly developed modulation doped transistors. Of greatest versatility is our c.w. mixing approach which can test systems up to several hundred GHz. These experiments have been compared with results obtained using pulsed sources with good agreement.

Devices examined include commercial FETs, "state of the art" industrial FETs, HEMT structures fabricated as part of this program and newly developed high frequency Heterojunction bi-polar transistors. Many of the initial goals of this program have been achieved or are underway. Specifically:

1. Efficient mixing has been obtained in 0.5 and 0.25 micron commercial FETs at high frequencies. Injection locking has been demonstrated up to 20 GHz and is being extended to higher frequencies and bandwidths. Typical results at 5 GHz are indicated in Figure 1. The locking bandwidth seems to be limited to about 4 MHz and is under investigation.

2. Mixing in HEMT devices [1] has also been explored as a function of frequency and temperature. The improvement in transconductance and mobility as a function of temperature, shown in Figure 2, has been observed in the laser mixing efficiencies. New devices using the latest E-beam fabrication technology (0.20 micron gates) are now being developed and will be tested in this effort. These HEMT devices will be incorporated into hybrid oscillator circuits designed for operation at 90 GHz.

3. Tests have been made on a series of Heterojunction Bi-polar devices [2] using both mixing techniques and high speed pulsed semiconductor lasers. These experiments have confirmed the validity of the results from mixing experiments in the region of overlap. In addition, they have demonstrated that these heterojunction devices are circuit limited and are capable of extremely high frequency behavior. Traces of the response of these devices using sampling scopes are shown in Figure 3. In Figure 4 we show the response obtained for the highest frequency transistors using mixing techniques. At this point the measurement is limited by the test fixture's parasitics.

Current efforts are to mount these devices into circuits as oscillators and amplifiers at millimeter wave frequencies. In order to accomplish this we have constructed a six port system to measure the relevant "S" parameters. Shown in Figure 5 is a diagram of this system now being used at 90 GHz. After identifying the high frequency devices using optical techniques, we find that we now can effectively design and construct three terminal oscillators for the millimeter.

References

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2. P.M. Asbeck, et al., "MM Wave Performance of GaAs/AlGaAs Heterojunction Bipolar Transistors (Proceedings IEDM, San Francisco 1984).

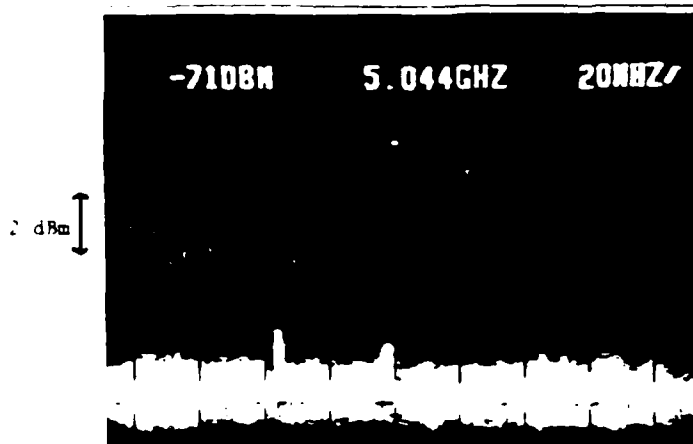


Figure 1a. Beat note approaches oscillator with $f = 5.044$ GHz.

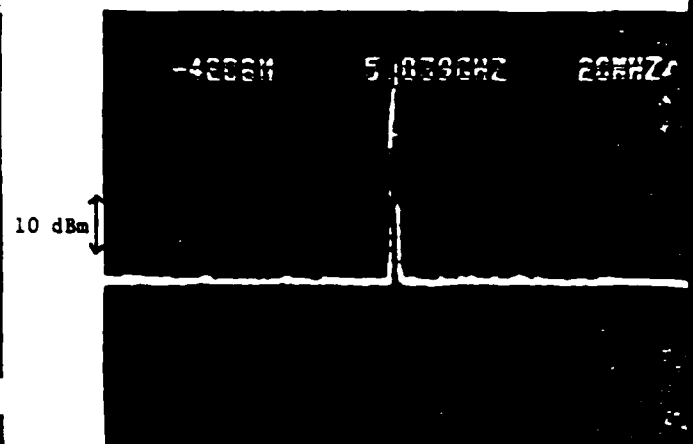


Figure 1b. Injection locking occurs with greatly increased power.

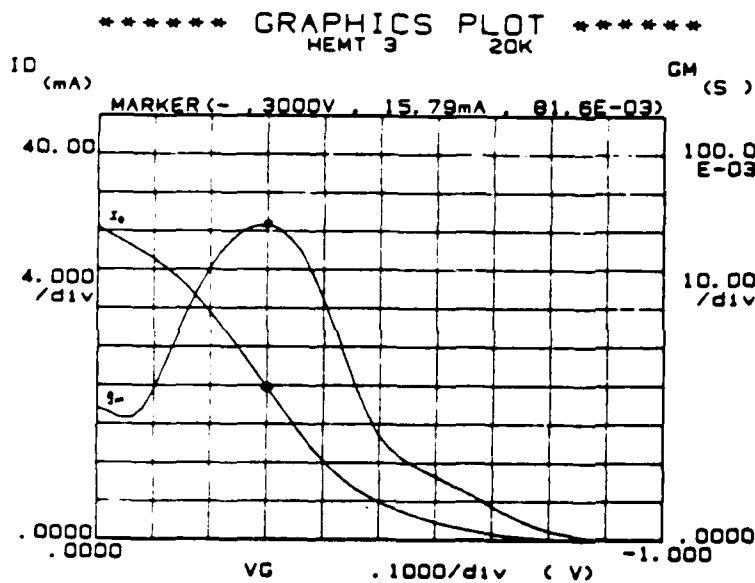


Figure 2. Transconductance at 20K showing the improvement in mobility of the GaAs 2D electron gas. This increase to over 260mS/mm is reflected in the optical mixing efficiency.

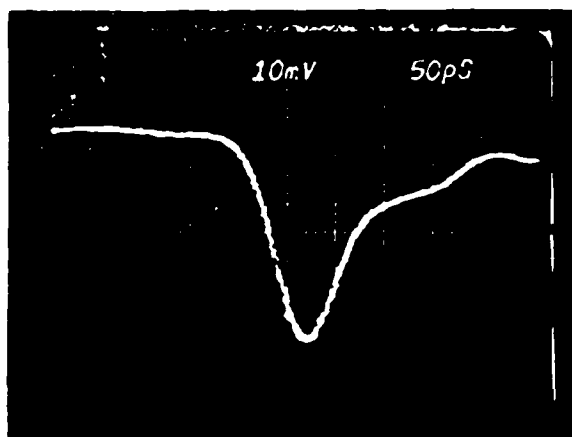


Figure 3a. Curve of semiconductor pulsed laser output using a fast photodiode showing a rise time of less than 50 ps. taken with a sampling scope.

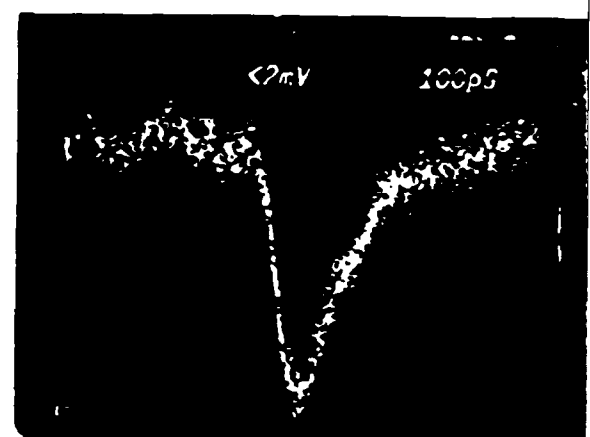


Figure 3b. Heterojunction Bi-polar's optical response. By injecting carriers directly into the base we are able to obtain intrinsic response measurements relatively independent of circuit parameters.

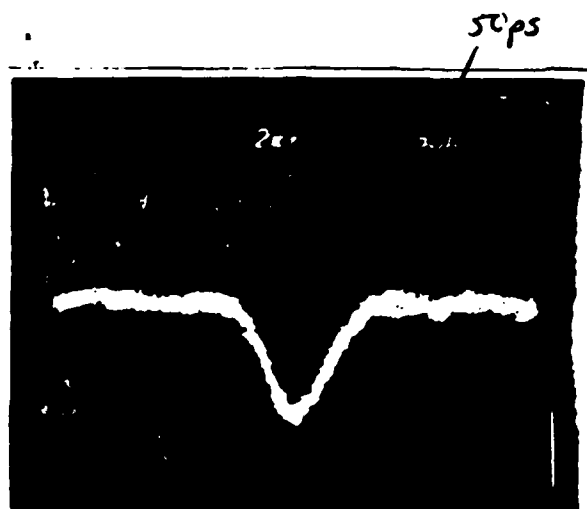


Figure 3c. The pulse response of a GaAlAs/GaAs FET tested using the laser diode.

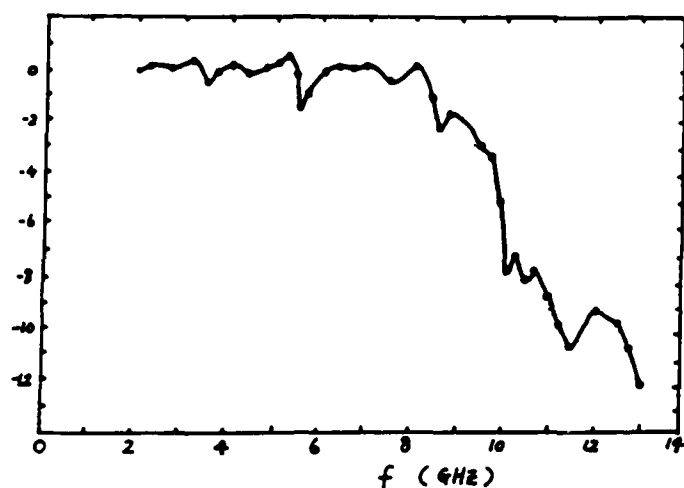


Figure 4. The frequency response measurement by the mixing experiment for the uniform band gap base transistor.

SIX-PORT NETWORK ANALYZER

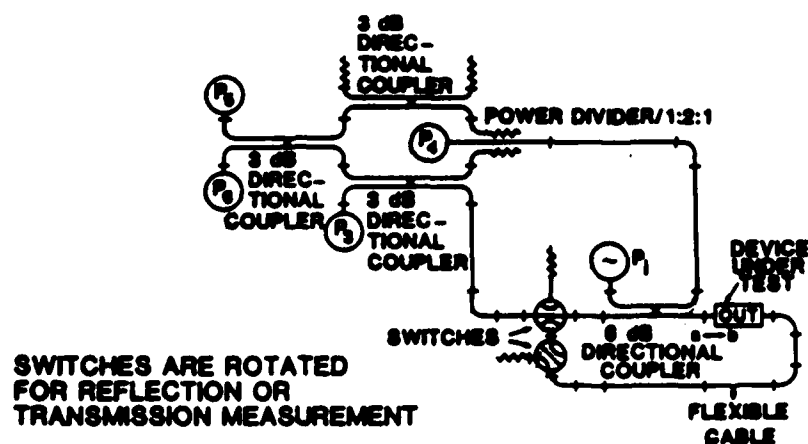


Figure 5. Six Port System to measure vector parameters at 90 GHz.

I. Introduction

In recent years the development of millimeter wave systems for missile guidance, radar, remote sensing, and communications has spurred advances in sources of millimeter waves. Traditional electronic tubes such as backward wave oscillators and klystrons are still used for high power applications [1]. In many millimeter wave systems such as airborne and satellite transmitters and receivers, space and weight are important design considerations. In such systems, solid state sources are preferred.

Today, two terminal solid state devices such as the IMPATT diode or the transferred-electron device (TED) provide power at millimeter wavelengths. IMPATT diodes can supply 2 W of continuous wave power at 40 GHz and several mW at 230 GHz [2]. Transferred-electron devices can produce about 20 mW at 100 GHz [3]. These devices require distributed feedback for sustained oscillation, therefore they must be encapsulated in a waveguide cavity.

The development of microwave integrated circuits suggests the application of similar technology to millimeter wave integrated circuits. For these planar circuits, either field-effect transistors or bipolar transistors would be the active element. In particular, the gallium arsenide metal-semiconductor field-effect transistor (GaAs MESFET) has had the most success to date as a millimeter wave planar source. MESFET oscillators have recently been constructed to operate at 69 GHz [4] and 110 GHz [5]. The power produced by these devices has been on the order of 1 mW. Even though millimeter wave transistors are presently low power devices, they could be used in low

noise amplifiers, local oscillators, mixers, and active elements for imaging arrays.

Recently, another type of field-effect transistor, which is called the either the high electron mobility transistor (HEMT) or the modulation-doped field-effect transistor (MODFET), has shown promise as a high frequency device. The HEMT is a consequence of research conducted on heterojunction superlattices [6]. The heterojunction superlattice is an arrangement of alternating layers of gallium arsenide and aluminum gallium arsenide ($\text{GaAs}/\text{Al}_x\text{Ga}_{1-x}\text{As}$). Each layer is typically 80 to 100 Å thick. The precise control needed to grow superlattice structures can be achieved with molecular-beam epitaxy. The $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layers are selectively doped with Si atoms, which are donors to $\text{Al}_x\text{Ga}_{1-x}\text{As}$. The conduction band edge of GaAs is lower in energy than the donor states in the $\text{Al}_x\text{Ga}_{1-x}\text{As}$ so that the free electrons from the $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layers move into the GaAs layers where they become confined. Since the mobile electrons are spatially separated from their parent donor atoms, ionized impurity scattering is significantly reduced. Thus, the electron mobility parallel to the heterojunction interfaces becomes enhanced, especially at low temperatures [7]. The highest reported low field electron mobility that was obtained at low temperatures was 2×10^6 cm²/V-sec [8].

Shubnikov-deHaas measurements on single $\text{GaAs}/\text{Al}_x\text{Ga}_{1-x}\text{As}$ interfaces has shown that the free electrons form a two-dimensional electron gas (2DEG) in the GaAs at the interface [9]. The HEMT consists of a single $\text{Al}_x\text{Ga}_{1-x}\text{As}/\text{GaAs}$ interface. A potential applied to a metal gate on top of the $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layer controls the charge density of the 2DEG thus modulating the current flow along the interface.

The first HEMT reported in the literature was fabricated by Mimura et. al. in 1980 [10]. Most of the research on HEMT's has been directed toward digital circuit applications. Here the HEMT has been shown to be superior in speed to the MESFET [8].

In the last few years, the microwave and millimeter wave properties of the HEMT have begun to be examined. Microwave HEMT's have been fabricated with gate lengths of 0.25 μm [11],[12]. The small-signal equivalent circuits of these transistors were determined from measured scattering parameters. From these circuits, unity current gain cut-off frequencies of 47 GHz and 70 GHz were computed. Furthermore, theoretical calculations from a model HEMT has placed the unity current gain cut-off frequency as high as 92 GHz and the maximum frequency of oscillation at 191 GHz [13]. HEMT's have also been applied to oscillators at 44 GHz and amplifiers from 35 GHz to 40 GHz [14].

The goal of this research is to further the characterization of HEMT's at microwave and millimeter wave frequencies. At microwave frequencies, the scattering parameters of a HEMT will be measured as a function of temperature. It is expected that the performance of the transistor will improve when it is cooled to very low temperatures. The microwave measurement will utilize an automatic network analyzer. The millimeter wave scattering parameters will be measured using a six-port network analyzer that was constructed for W-band (75 GHz to 110 GHz). Finally, with the data obtained from these measurements, a HEMT millimeter wave oscillator will be designed and fabricated.

II. HEMT CONSTRUCTION

The HEMT consists of single GaAs/ $\text{Al}_x\text{Ga}_{1-x}\text{As}$ heterojunction. The $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layer is doped with Si to provide donor atoms with a concentration of about 10^{18}cm^{-3} . A spacer layer of undoped $\text{Al}_x\text{Ga}_{1-x}\text{As}$ which is 20 Å to 100 Å thick separates the GaAs and the n-type $\text{Al}_x\text{Ga}_{1-x}\text{As}$. The undoped GaAs channel is grown on a semi-insulating of LEC substrate.

The electron affinity for GaAs is larger than that for $\text{Al}_x\text{Ga}_{1-x}\text{As}$. As a result, a potential well is formed in the GaAs next to the $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layer, as shown in figure 1. Free electrons from the doped $\text{Al}_x\text{Ga}_{1-x}\text{As}$ region fall into the sub-bands of the potential well. Since the potential well is narrow, the electrons form a two-dimensional electron gas (2DEG). The electron density of the 2DEG at equilibrium is

$$n_s = D kT \log \left[\left(1 + e^{q(E_F - E_0)/kT} \right) \left(1 + e^{q(E_F - E_1)/kT} \right) \right]$$

where D is the two-dimensional density of states, E_F is the equilibrium Fermi level, and E_0 and E_1 are the first two sub-band energy levels [15].

The actual construction of a typical transistor is shown in figure 2. To understand the physical operation of the HEMT, the gradual channel approximation is used in the following explanation. When a negative potential is applied to the gate and a positive potential is applied to the drain, the 2DEG concentration varies as a function of gate voltage and the channel voltage as

$$Q_s(x) = q n_s(x) = \frac{\epsilon_2}{d_2} (V_G - V_c(x) - V_{off})$$

where ϵ_1 is the dielectric constant of $\text{Al}_x\text{Ga}_{1-x}\text{As}$, d_1 is the thickness of the $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layer and V_{off} is the turn-off voltage which is related to the material properties of the heterojunction and the device structure.

The current in the channel is

$$I = Q_s(x) Z v(x)$$

where Z is the gate width and $v(x)$ is the electron velocity at x , the distance along the channel from the source. The electric fields along the channel can be large enough for the electron velocity to saturate at a critical field strength \mathcal{E}_c . If the electron velocity is approximated by

$$v = \begin{cases} \mu \mathcal{E} & \mathcal{E} < \mathcal{E}_c \\ v_s & \mathcal{E} \geq \mathcal{E}_c \end{cases}$$

with μ being the low field electron mobility and v_s the electron saturation velocity, then it can be shown that the saturation current for a short gate HEMT is given by

$$I_s = \frac{Z \epsilon_1 v_s}{d_1} (V_G - V_{\text{off}} - R_s I_s - \mathcal{E}_c L)$$

where L is the gate length and R_s is the source resistance. The transconductance is defined as

$$g_m = \frac{\partial I_D}{\partial V_G} = \frac{Z \epsilon_1 v_s}{d_1}$$

Since the electrons are physically removed from their donor ions, the

value of v_s should be that of intrinsic GaAs. The saturation velocity increases as the device is cooled because electron-phonon collisions decrease. Therefore, the transconductance should increase as the device is cooled. This is indeed what was observed on a 0.5 μm gate length HEMT as shown in figures 3 and 4.

III. MICROWAVE MEASUREMENTS

A. Figures of Merit

The small-signal equivalent circuit of an FET is shown in figure 5 [16]. The relationship of the lumped model components to the physical HEMT structure is shown in figure 6. The small-signal model is used to calculate various figures of merit of the transistor. Of particular importance is the transistor's stability, maximum available gain, and the maximum frequency of oscillation [17],[18],[19]. These quantities may be calculated by considering the transistor as a two-port circuit with the source terminal grounded, the gate terminal as the input, and the drain terminal as the output. From the equivalent circuit, the admittance parameters, which relate the input and output currents to the input and output voltages, can be determined. Also, the scattering parameters can be derived from the admittance parameters, and vice versa. These relationships are shown in figure 7.

The internal feedback of the transistor may cause it to oscillate even in the absence of external feedback. A stability factor k is given by

$$k = \frac{2 \operatorname{Re} y_{11} \cdot \operatorname{Re} y_{22} - \operatorname{Re}(y_{12} y_{21})}{|y_{12}| |y_{21}|}$$

When the stability factor is greater than one, the transistor is unconditionally stable. This means that the transistor will not suffer internal oscillations regardless of the source or load admittance. If k is between -1 and 1 , the transistor is conditionally stable. In this case stability may be achieved under some source and load admittances.

When the transistor is unconditionally stable, the maximum available gain (MAG) is calculated from

$$\text{MAG} = \left| \frac{y_{21}}{y_{12}} \right| \frac{1}{K + \sqrt{K^2 - 1}}$$

The MAG is the power gain of the transistor when the source and load terminations are conjugates to the two-port input and output admittances, respectively. If the transistor is conditionally stable, the unilateral gain (U) is the maximum available gain of the device when an external feedback circuit has eliminated y_{12} . It is given by

$$U = \frac{|y_{21} - y_{12}|^2}{4 (\operatorname{Re} y_{11} \cdot \operatorname{Re} y_{22} - \operatorname{Re} y_{12} \cdot \operatorname{Re} y_{21})}$$

MAG and U have unity gain at the maximum frequencies of oscillation f_{\max} and f_u .

B. Experiment

The two-port scattering parameters of a $0.25 \mu\text{m}$ gate length HEMT that

was fabricated by TRW were measured. The transistor was mounted on a chip carrier and wire bonded to 50 Ω microstrip transmission lines. Flange mount jack receptacles (Omni Spectra #2052-5636-02) were silver epoxied to the microstrip lines to provide transitions to SMA type connectors. The chip carrier was fastened to a copper cold finger that was located in a gas helium refrigerator. A Hewlett-Packard 8510 Automatic Network Analyzer was used to measure the scattering parameters of the HEMT from 2 GHz to 15 GHz. The temperature of the transistor was varied from 297 K to 20 K.

The small-signal equivalent circuit of the transistor was calculated from the scattering parameter values over the frequency range by using the optimization routines available on Super-Compact[®], the microwave circuit analysis program [20]. The HEMT gate bias voltage, for a given drain voltage, was located at the point of maximum transconductance as determined from the DC current-voltage curves obtained by a semiconductor parameter analyzer. Figure 8 shows the small-signal equivalent circuits of the HEMT at 297 K and 20 K. The maximum frequencies of oscillation increased from 75 GHz to 87 GHz as the transistor was cooled. It should also be noted that the transconductance increased from 20 mS to 27 mS.

IV. MILLIMETER WAVE MEASUREMENTS

A. Six-Port Network Analyzer

The microwave measurements indicate that the 0.25 μm gate length HEMT is capable of providing gain up into W-band frequencies. However, to understand the device operating characteristics at these frequencies it is necessary to measure the scattering parameters of the HEMT directly. To accomplish this task, a W-band six-port network analyzer [21] has been constructed.

The general six-port junction is shown in figure 9. Since the six-port junction is linear, the waves travelling away from the junction can be related to the waves travelling toward the junction by 36 scattering parameters. The outgoing wave at the i th port can be written as

$$b_i = \sum_{j=1}^6 S_{ij} a_j \quad i = 1, \dots, 6$$

where a_j is the incoming wave at the j th port and S_{ij} is the scattering parameter that relates the wave coming out of port i to an input wave at port j when all other ports are match terminated. Power meters are attached to four of the six ports. Each power meter presents a reflection coefficient to the junction port with which it is connected, so that

$$a_j = b_j \Gamma_j^p \quad j = 3, \dots, 6$$

Thus, there exists ten equations describing the six-port and twelve unknowns (a_j, b_j). The system of equations may be solved for ten of the variables in terms of the two remaining variables as

$$b_i = A_i a_2 + B_i b_2 \quad i=3, \dots, 6$$

where A_i and B_i are functions of the S_{ij} 's and the Γ_i^P 's.

The power measured at the i th port is given by

$$P_i = |b_i|^2 = |A_i|^2 |a_2|^2 + A_i B_i^* b_2 a_2^* + A_i^* B_i b_2^* a_2 + |B_i|^2 |b_2|^2 \quad i=3, \dots, 6$$

The quantities $|b_2|^2$, $b_2 a_2^*$, $b_2^* a_2$, and $|a_2|^2$ may be solved in terms of the measured powers P_i . If an unknown load is placed on port 2 with the signal source located at port 1, then

$$\Gamma_L = \frac{a_2}{b_2} = \frac{b_2^* a_2}{|b_2|^2} = \frac{\sum_{i=3}^6 (x_i + j y_i) P_i}{\sum_{i=3}^6 \rho_i P_i}$$

where x_i , y_i , and ρ_i are functions of all A_i and B_i . In practice, x_i , y_i , and ρ_i are determined by calibration where known loads are placed on port 2 and the measured powers are recorded.

For operation at W-band, the six-port network analyzer was constructed from WR-10 waveguide components. A schematic of the system is shown in figure 10. Two waveguide switches enable the six-port network analyzer to measure transmission coefficients as well as reflection coefficients. The calibration of the six-port can be done with four offset short circuits as the load standards [22]. The six-port is currently being programmed for data recording, calibration, and measurement.

B. HEMT Scattering Parameter Measurement

To measure the W-band scattering parameters of the HEMT, it is necessary to build waveguide to microstrip transitions. A ridged waveguide transition was chosen for optimum bandwidth. The transition was designed to produce a Chebyshev impedance transformation from WR-10 waveguide (cross-section 0.1" x 0.05") to 50 microstrip transmission lines [23], [24]. A drawing of the transition is shown in figure 11. A test fixture consisting of two transitions separated by a length of microstrip line between them has been constructed. The microstrip lines are 0.011" wide and were fabricated on 0.005" thick quartz substrates [25]. Small microstrip loads were machined from waveguide absorber (Eccosorb MF117). The return loss of a transition was measured and it is shown in figure 12. It indicates an average return loss of better than 15 dB across the band.

For millimeter wave characterization, the HEMT will be centered in the test fixture between the two transitions. The gate and drain will be bonded to the microstrip lines. DC voltage will be applied through a bias circuit. The six-port will be calibrated in waveguide. Next the test fixture including transitions and the HEMT will be tested. Then, by measuring the return loss of the transition and the insertion loss of the test fixture with a straight through length of microstrip line, the scattering parameters of the HEMT can be de-embedded from the measured scattering parameters.

IV. MILLIMETER WAVE HEMT OSCILLATOR

A prototype 94 GHz HEMT oscillator was designed and fabricated. Since no HEMT microwave data was available, a small-signal equivalent circuit of a microwave 0.25 μm gate length GaAs MESFET was supplied by Hughes Aircraft Co. Research Laboratory. The model was derived from measured S-parameters up to 26 GHz. It was felt that the high frequency MESFET would be similar in operation to a high frequency HEMT, so that the design of a prototype oscillator was attempted.

The oscillator circuit was designed using Super-Compact[®]. An impedance on the drain and the source terminals of the transistor was varied until the maximum negative resistance obtainable at the gate terminal was found. Then, the reactance at the gate was tuned out by placing a conjugate reactance at the gate. As figure 13 shows, inductances were required at all terminals of the transistor. A negative input resistance of -18Ω at 94 GHz was then available at the gate when the drain terminal load resistance was 200Ω .

The circuit realization is shown in figure 14. All circuit components were fabricated on 0.005" fused silica substrates using a gold up-plating process. The inductances were realized by open circuited microstrip stubs. The circuit pieces were bonded around a TRW 0.25 μm gate length HEMT.

Since the circuit was to be placed inside the vacuum jacket of the gas helium refrigerator, an electromagnetically coupled dipole antenna was used as the output load [26]. A signal generated by the oscillator would then be radiated out through a window to a detector outside the vacuum jacket. A model

of the dipole was tested between 6 GHz and 9 GHz to determine the proper length and position from the feed point to provide a 50Ω match. The model was then scaled down so that the dipole would be resonant at 94 GHz. The antenna dimensions are shown in figure 14.

Upon testing the oscillator, no signal was observed at room temperature. However, at 50 K a 35 GHz signal was detected. Altering the circuit external to the transistor did not change theis oscillation frequency. It was concluded that the oscillation was due to the transistor's internal feedback. It may be possible to eliminate this unwanted oscillation through the use of a proper size chip capacitor in the bias line to provide a 35 GHz ground.

The prototype oscillator suffered two major drawbacks. One was the inability to experimentally tune the oscillator. The other was the use of the electromagnetically coupled dipole which at these frequencies is too small to successfully find the location from the feedpoint for an impedance match. Figure 15 is a sketch of an improved HEMT oscillator design. Tuning is accomplished by using bond wires or ribbons between small gold patches as a means of altering the length of the microstrip stubs. Then, instead of the electromagnetically coupled dipole at the output, a microstrip transition to a 50Ω line is inserted so that the waveguide to microstrip transition can be incorporated in the design. Of course, the final design would depend on the measured scattering parameters of the HEMT.

VI. CONCLUSION - FUTURE WORK

The microwave measurements on the 0.25 μm gate length HEMT have indicated its ability to provide gain at millimeter wave frequencies. Refined microwave measurements are planned. Recently, a new SMA to microstrip connector (Wiltron K connector) that has a 15 dB or greater return loss up to 40 GHz has become commercially available. Therefore, it is planned to have a new microwave test fixture fabricated with these new connectors. Also, the test fixture will be made long enough to enable the use of time domain reflectometry (TDR) which is built in to the Automatic Network Analyzer. TDR allows one to record the frequency response of a device and test fixture, then to window out the test fixture connectors in the time domain, and finally to convert back into the frequency domain to obtain the response of the device alone.

When the six-port network analyzer is finally programmed to calibrate and measure reflection and transmission coefficients, its operation will be verified by inserting a load consisting of a variable phase shifter and a variable attenuator. The accuracy of the attenuator is specified to be within 0.1 dB, and the accuracy of the phase shifter is specified to be within 4° .

After the six-port network analyzer operation is verified, the scattering parameters of the 0.25 μm gate length HEMT can be measured. A new waveguide to microstrip test fixture must be built to assure the critical alignment of the HEMT with the microstrip and the microstrip with the ridged waveguide of the transitions.

Finally, a millimeter wave HEMT oscillator will be designed using the

measured scattering parameters. If the HEMT is characterized at a few frequencies within the W-band then the small-signal equivalent circuit of the millimeter wave HEMT can be obtained. This would then be the transistor model used to optimize the oscillator circuit design.

The advances in microwave and millimeter wave electronics that would be obtained from this research project is summarized in the following list:

1. The HEMT will be characterized at microwave frequencies as a function of temperature.
2. The HEMT will be evaluated at millimeter wavelengths (W-band).
3. Scattering parameters of an active device will be made using a six-port network analyzer.
4. A millimeter wave HEMT oscillator will be designed from measured W-band scattering parameters.

The conclusions drawn from this work should define and stimulate future research on the HEMT as a millimeter wave circuit element.

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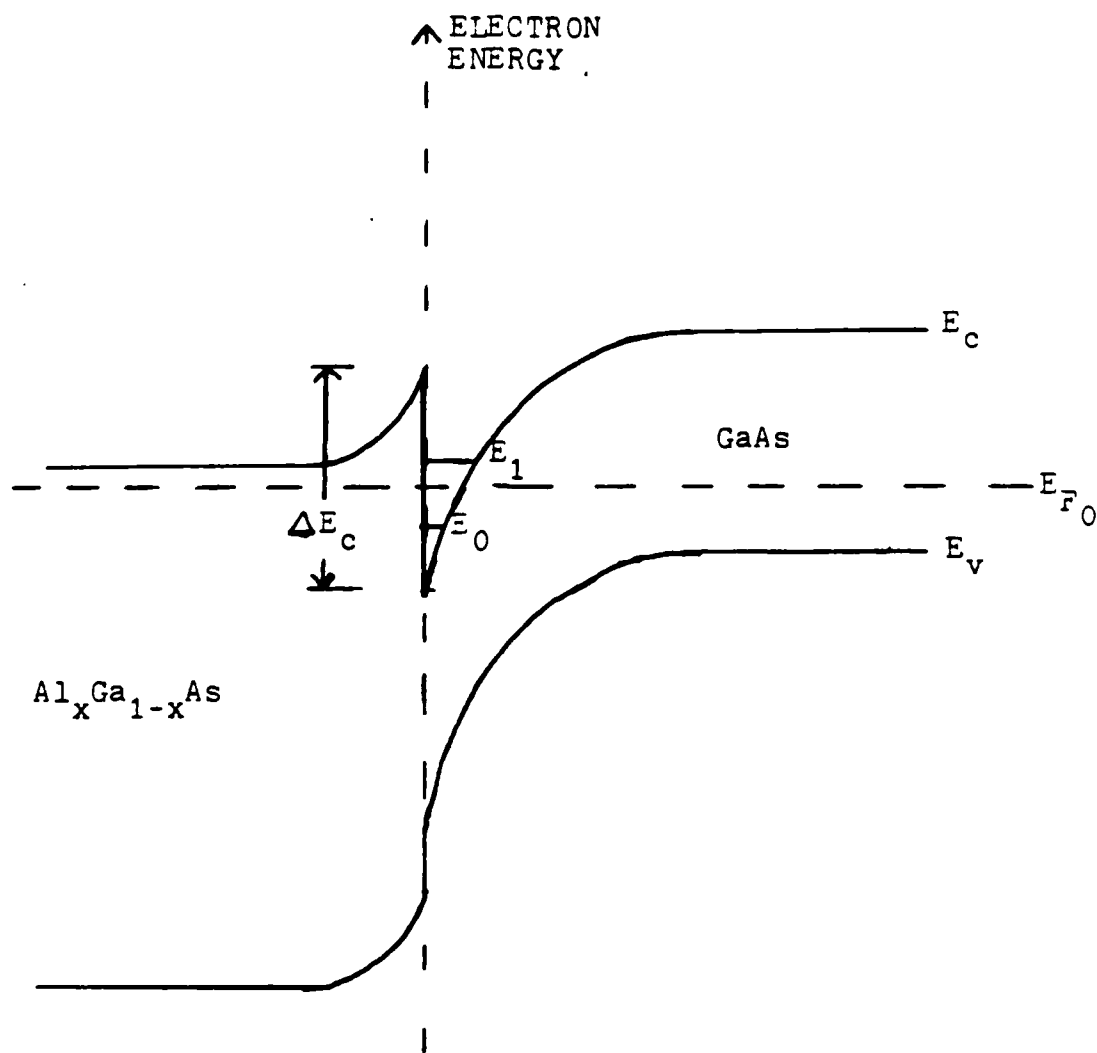


Figure 1. $\text{Al}_x\text{Ga}_{1-x}\text{As}/\text{GaAs}$ Heterojunction at Equilibrium

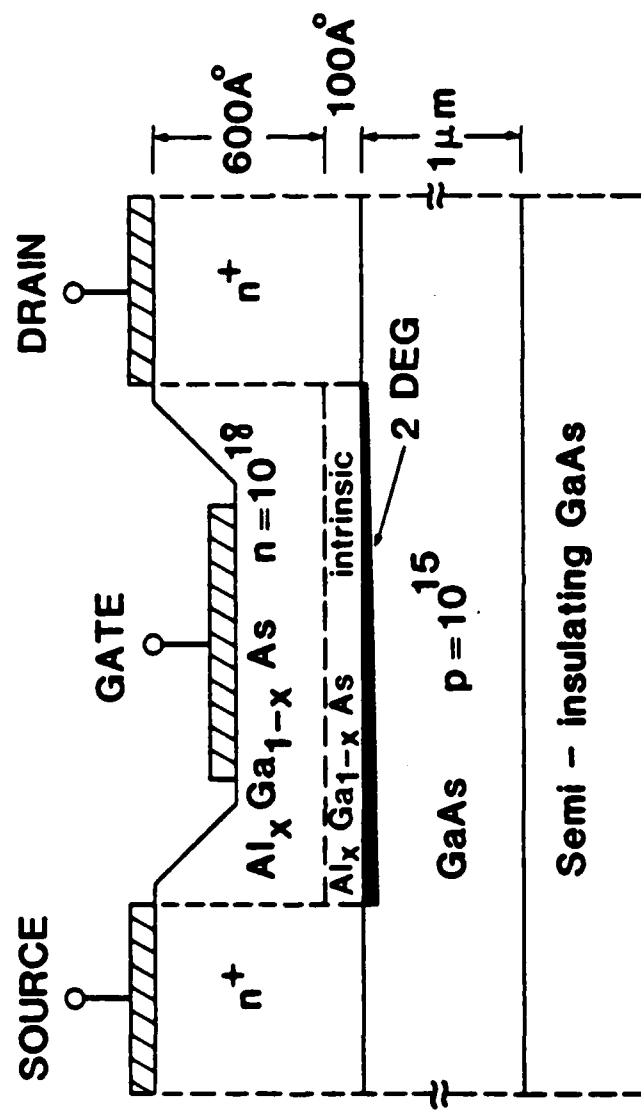
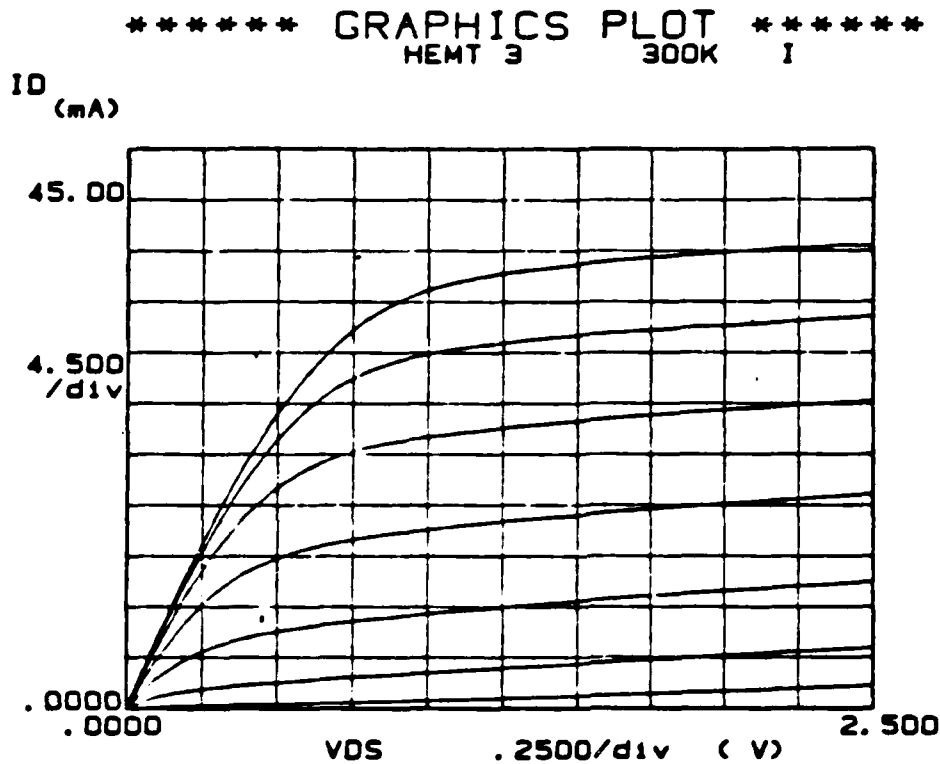


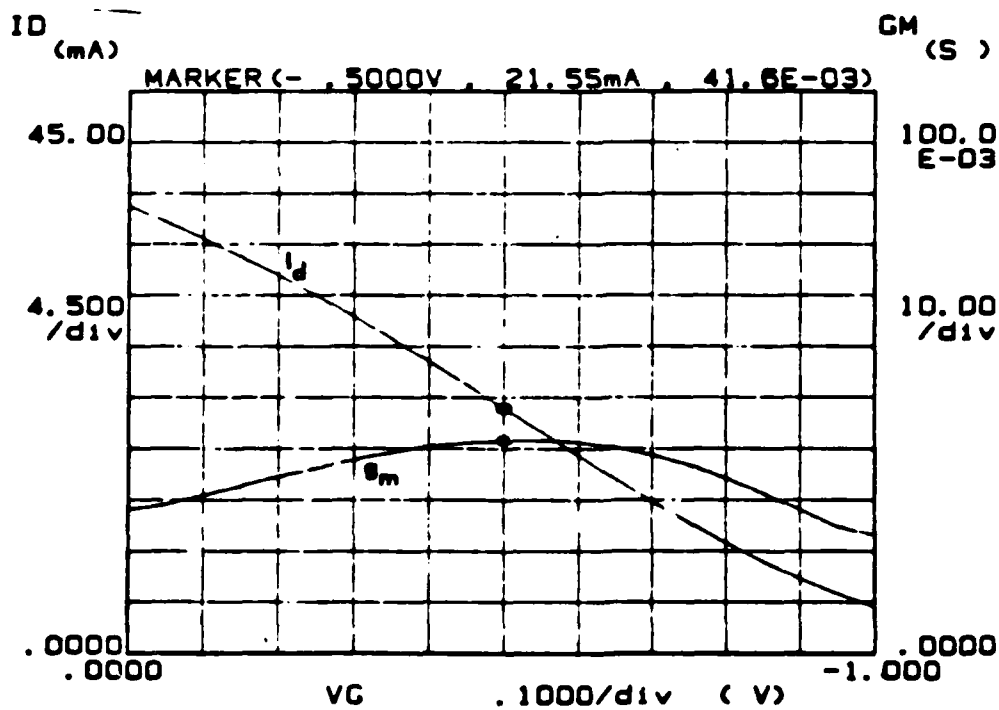
Figure 2. Typical HEMT Cross Section



Variable1:
VDS -Ch2
Linear sweep
Start .0000V
Stop 2.5000V
Step .1000V

Variable2:
VG -Ch3
Start .0000V
Stop -1.2000V
Step -.2000V

Constants:
VS -Ch1 .0000V



Variable1:
VG -Ch3
Linear sweep
Start .0000V
Stop -1.0000V
Step -.0500V

Variable2:
VDS -Ch2
Start 1.5000V
Stop 1.5000V
Step .0000V

Constants:
VS -Ch1 .0000V

GM (S) = $\Delta ID / \Delta VG$

Figure 3. 0.5 um Gate Length HEMT - 300 K

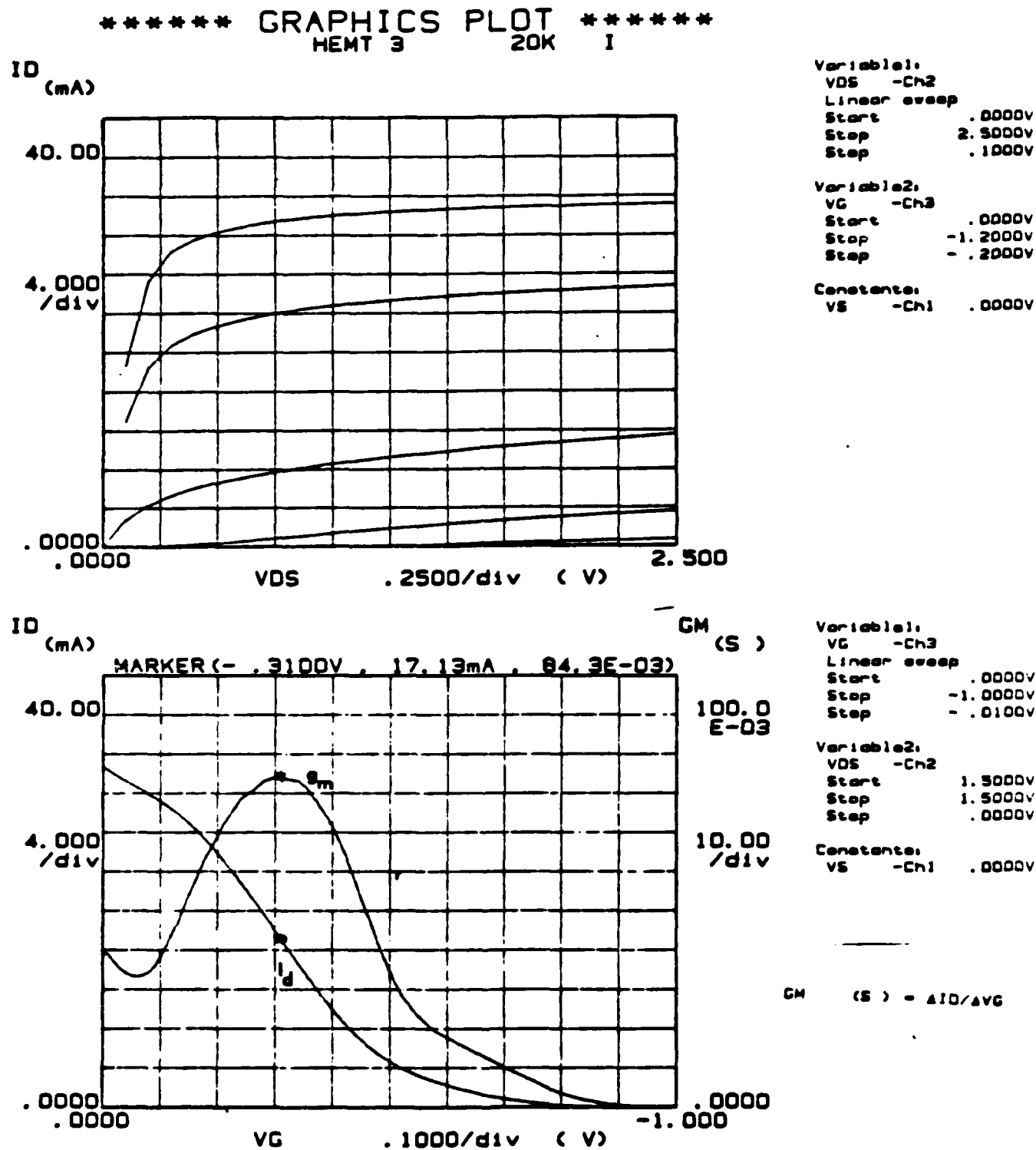


Figure 4. 0.5 μ m Gate Length HEMT - 20 K

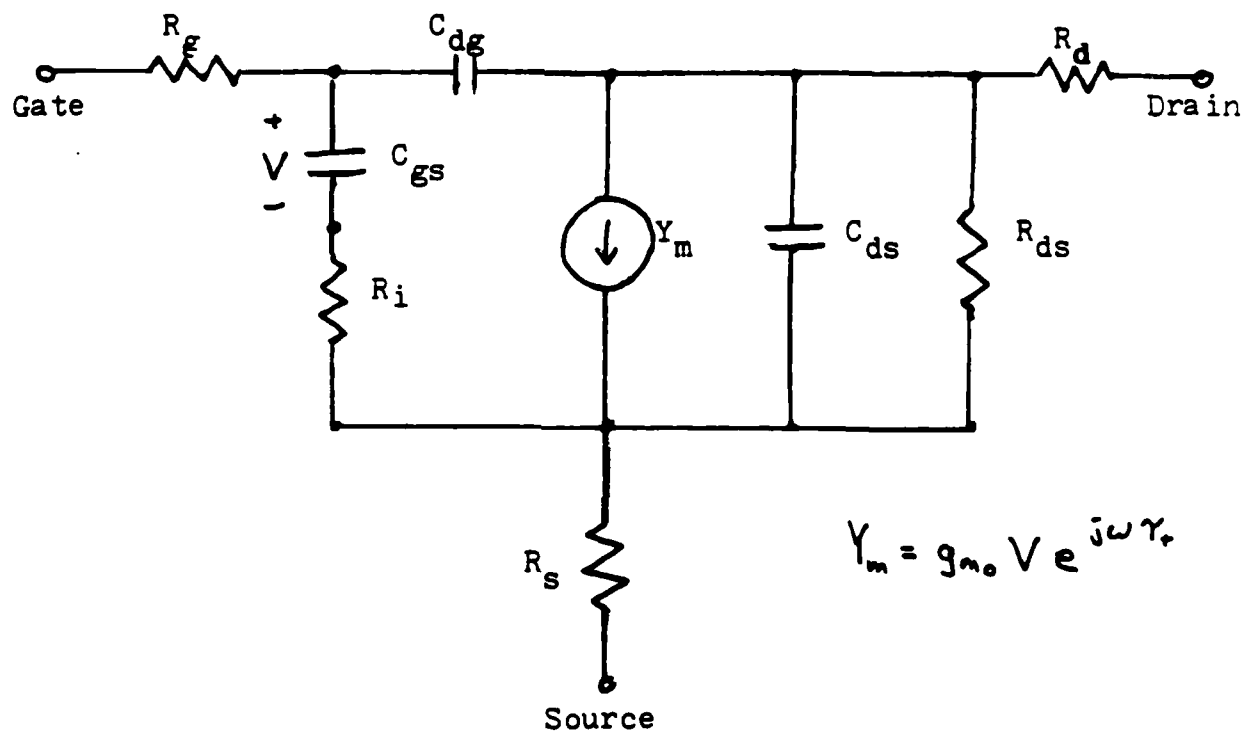
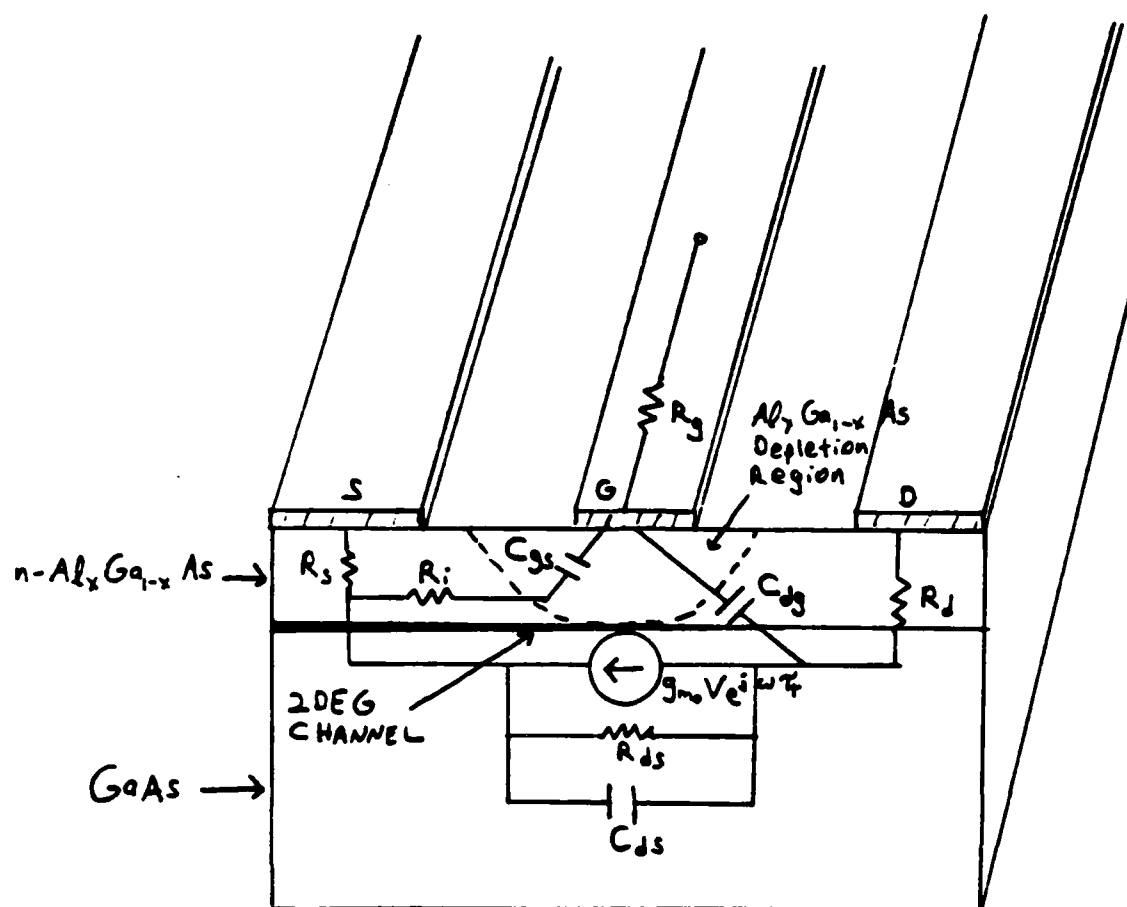
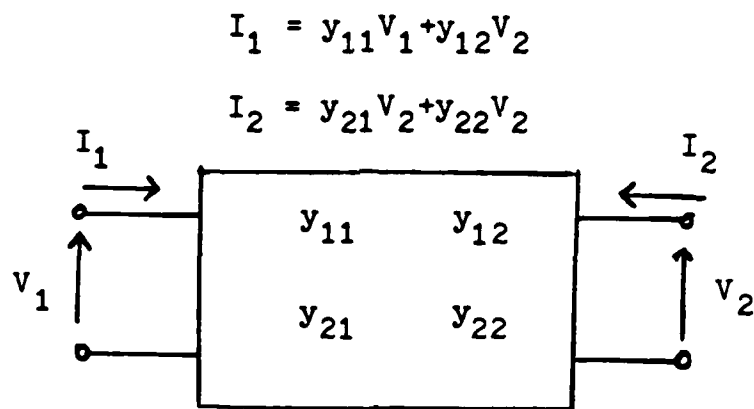


Figure 5. Small-Signal Equivalent Circuit

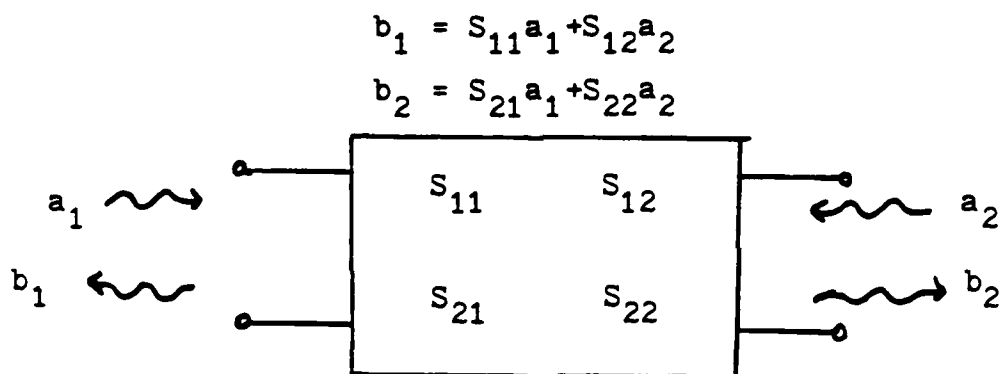


- g_{m0} - transconductance
- R_{ds} - channel output resistance
- C_{ds} - channel dipole layer capacitance
- C_{gs} - gate-to-source capacitance
- C_{dg} - gate-to-drain feedback capacitance
- R_i - channel input resistance
- R_g - gate metallization resistance
- R_s, R_d - source and drain contact resistance and bulk resistance
- τ_f - channel phase delay

Figure 6. Physical HEMT Model



a) Admittance Parameters



b) Scattering Parameters

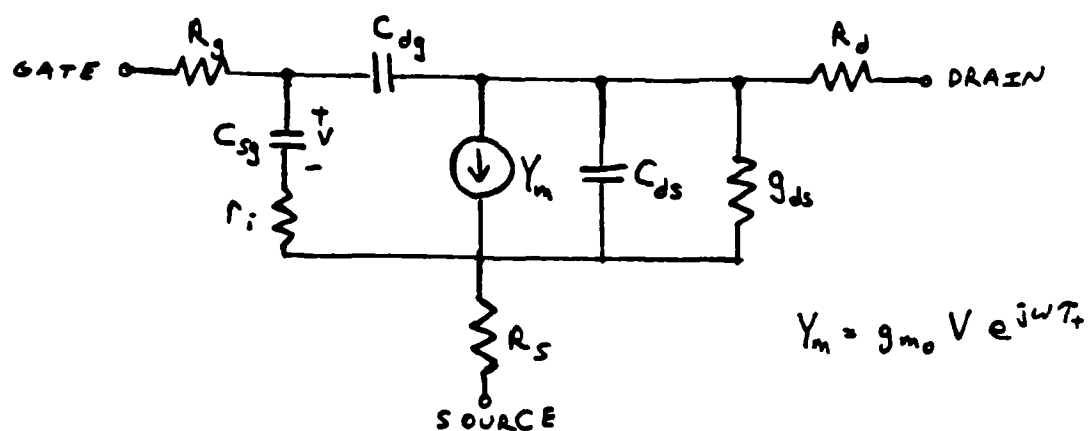
$$y_{11} = \frac{S_{12}S_{21} + (1 - S_{11})(1 + S_{22})}{-S_{12}S_{21} + (1 + S_{11})(1 + S_{22})} \cdot \frac{1}{Z_0}$$

$$y_{12} = \frac{-2S_{12}}{-S_{12}S_{21} + (1 + S_{11})(1 + S_{22})} \cdot \frac{1}{Z_0}$$

$$y_{21} = \frac{-2S_{21}}{-S_{12}S_{21} + (1 + S_{11})(1 + S_{22})} \cdot \frac{1}{Z_0}$$

$$y_{22} = \frac{S_{12}S_{21} + (1 + S_{11})(1 - S_{22})}{-S_{12}S_{21} + (1 + S_{11})(1 + S_{22})} \cdot \frac{1}{Z_0}$$

Figure 7. Two-Port Admittance and Scattering Parameters



	297 K	20 K
V_{ds}	2.5V	2.5 V
V_g	-0.05V	-0.15 V
g_{m0}	0.02033 S	0.02765 S
C_{sg}	0.164 pS	0.185 pF
T_t	1.82×10^{-12} sec	12.1×10^{-12} sec
C_{dg}	460 pF	1.5×10^{-3} pF
g_{ds}	1.95×10^{-3} S	2.0×10^{-3} S
r_i	1.558	0.441
r_g	0.0431	0.0925
r_d	0.6374	0.211
r_s	7.104	8.61
C_{ds}	2.31 pF	0.0164 pF
f_t	20 GHz	23.7 GHz
f_u	75.2 GHz	87.7 GHz

Figure 8. HEMT Equivalent Circuits at 297 K and 20 K

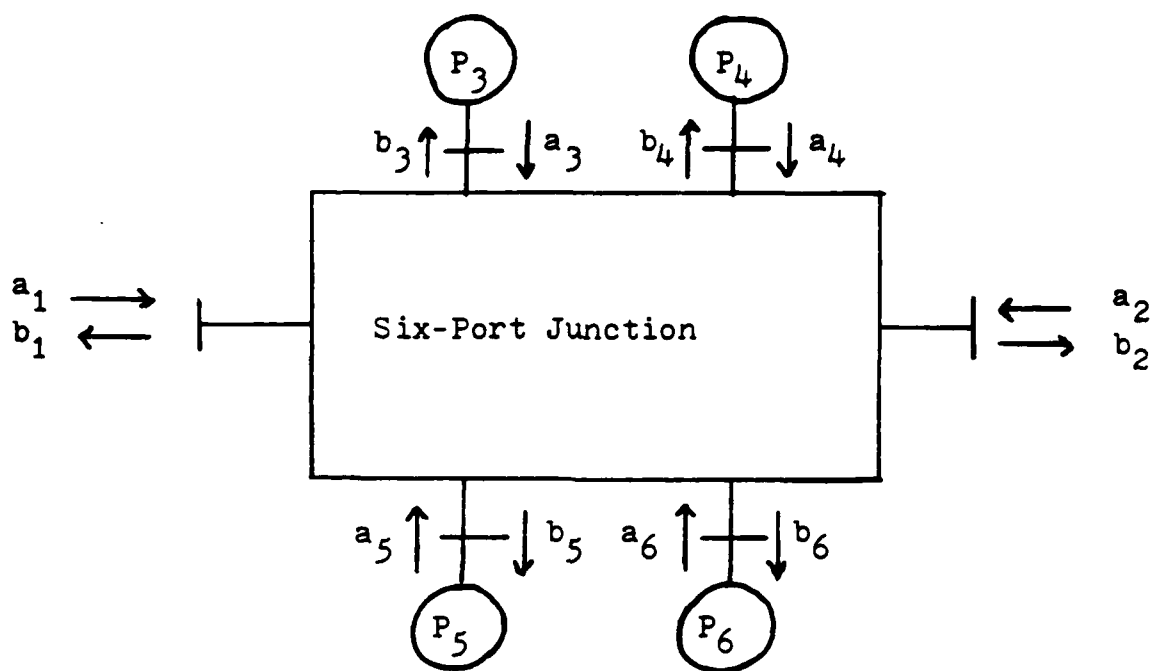


Figure 9. General Six-Port Junction

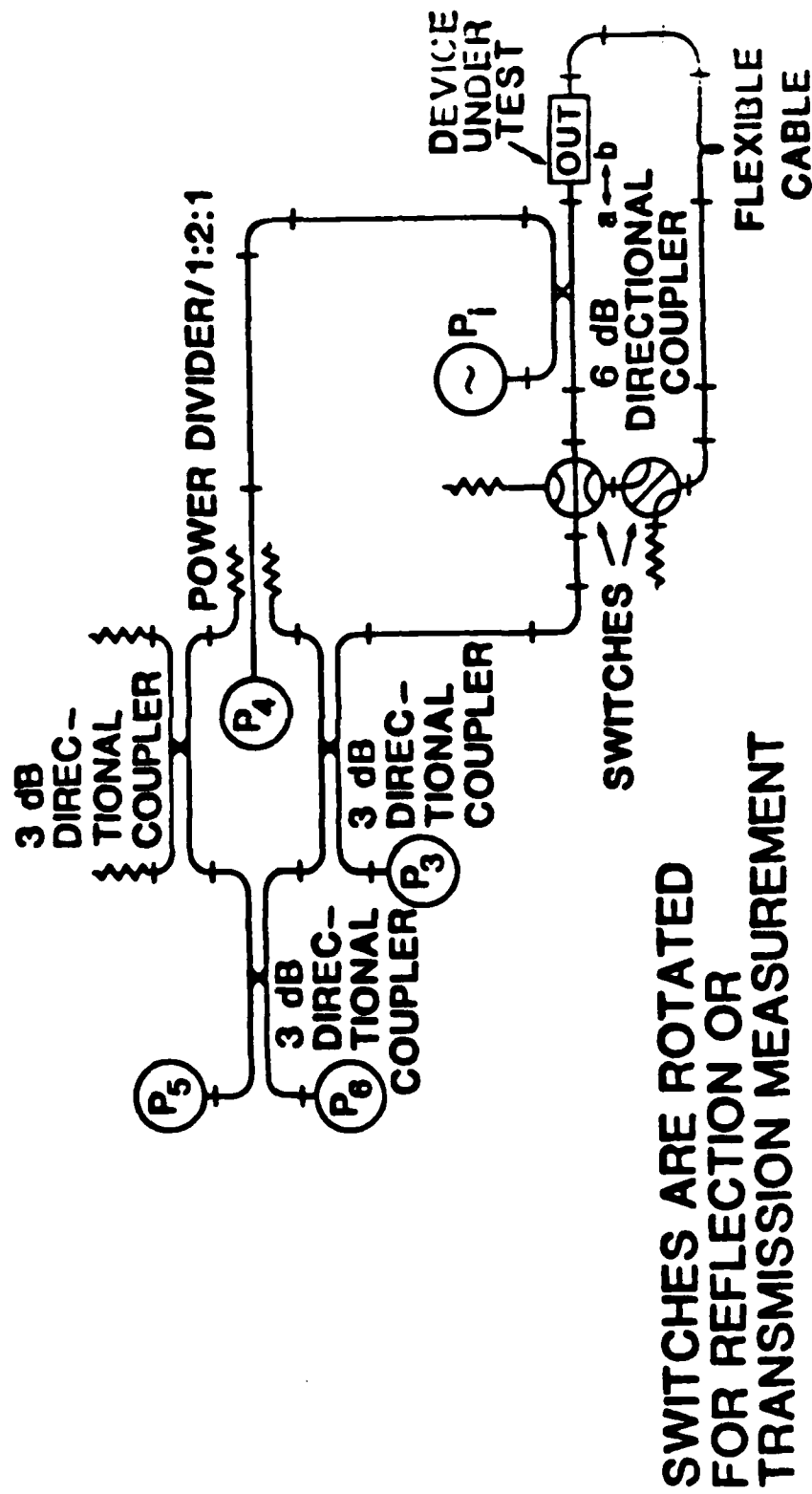


Figure 10. Six-Port Network Analyzer

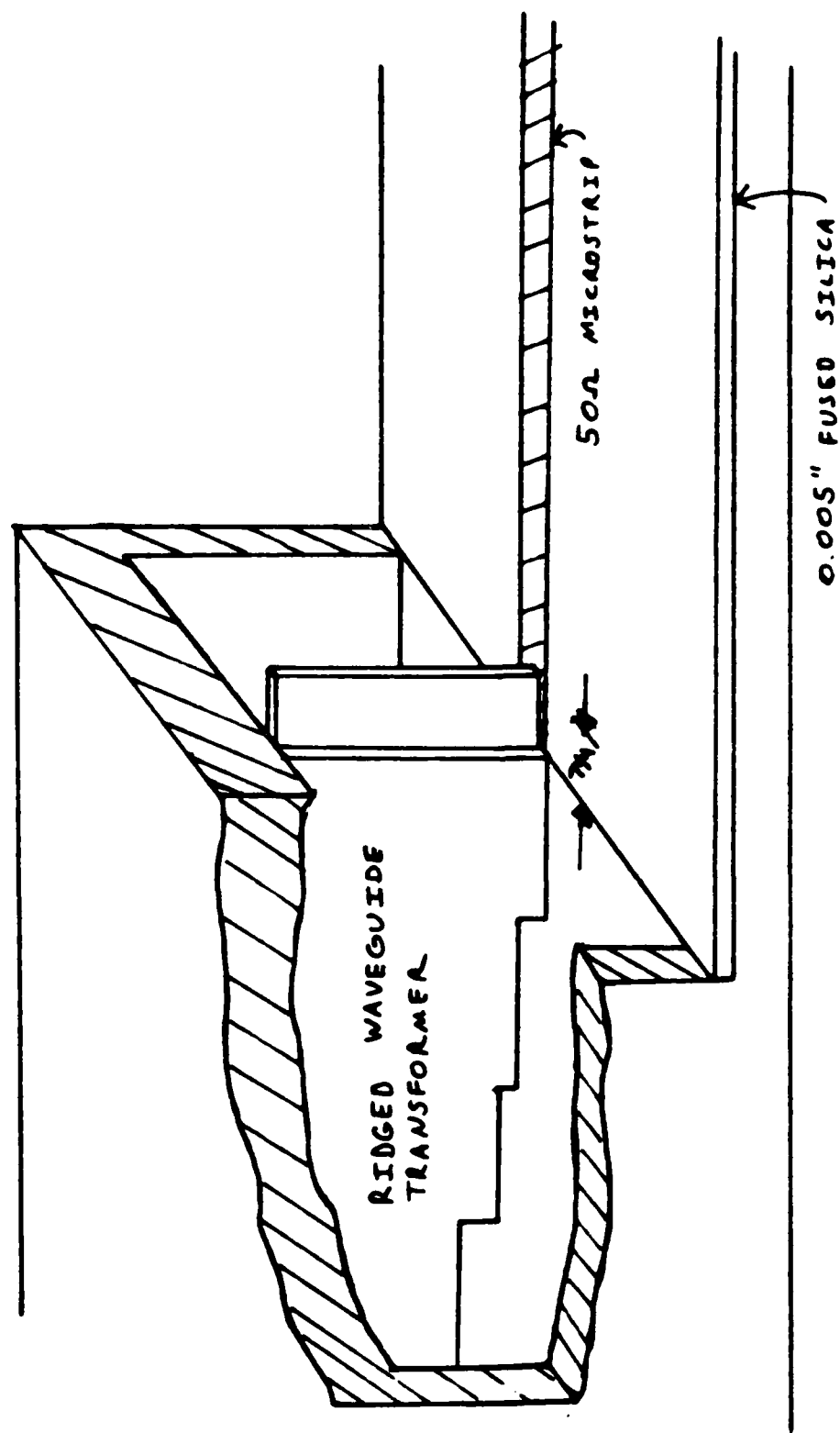


Figure 11. Waveguide to Microstrip Transition

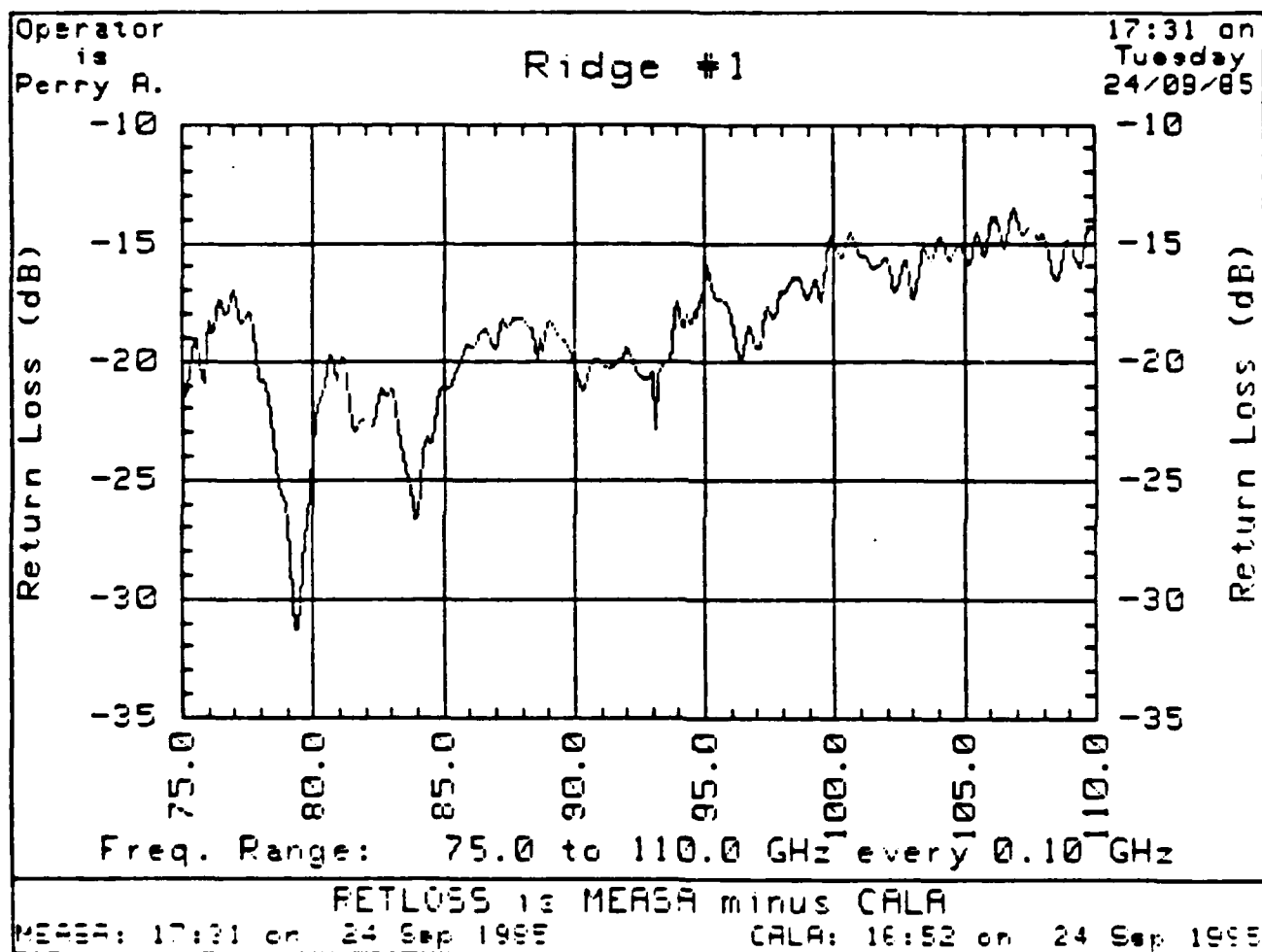


Figure 12. Return Loss of the Transition

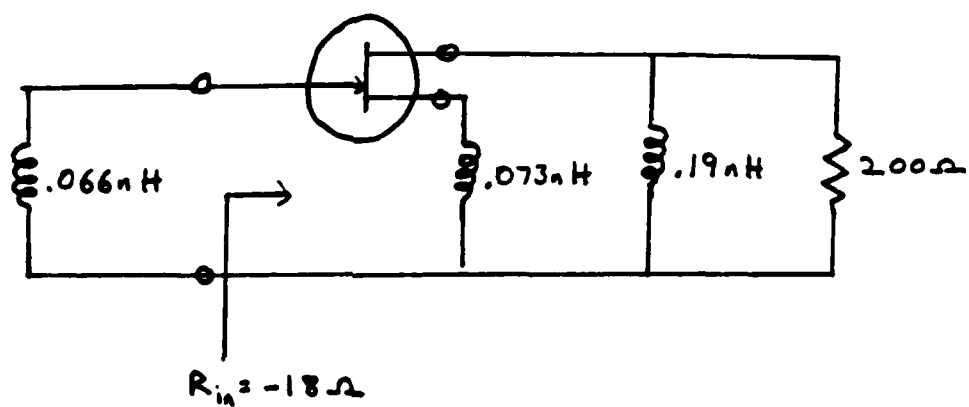


Figure 13. HEMT Oscillator Schematic

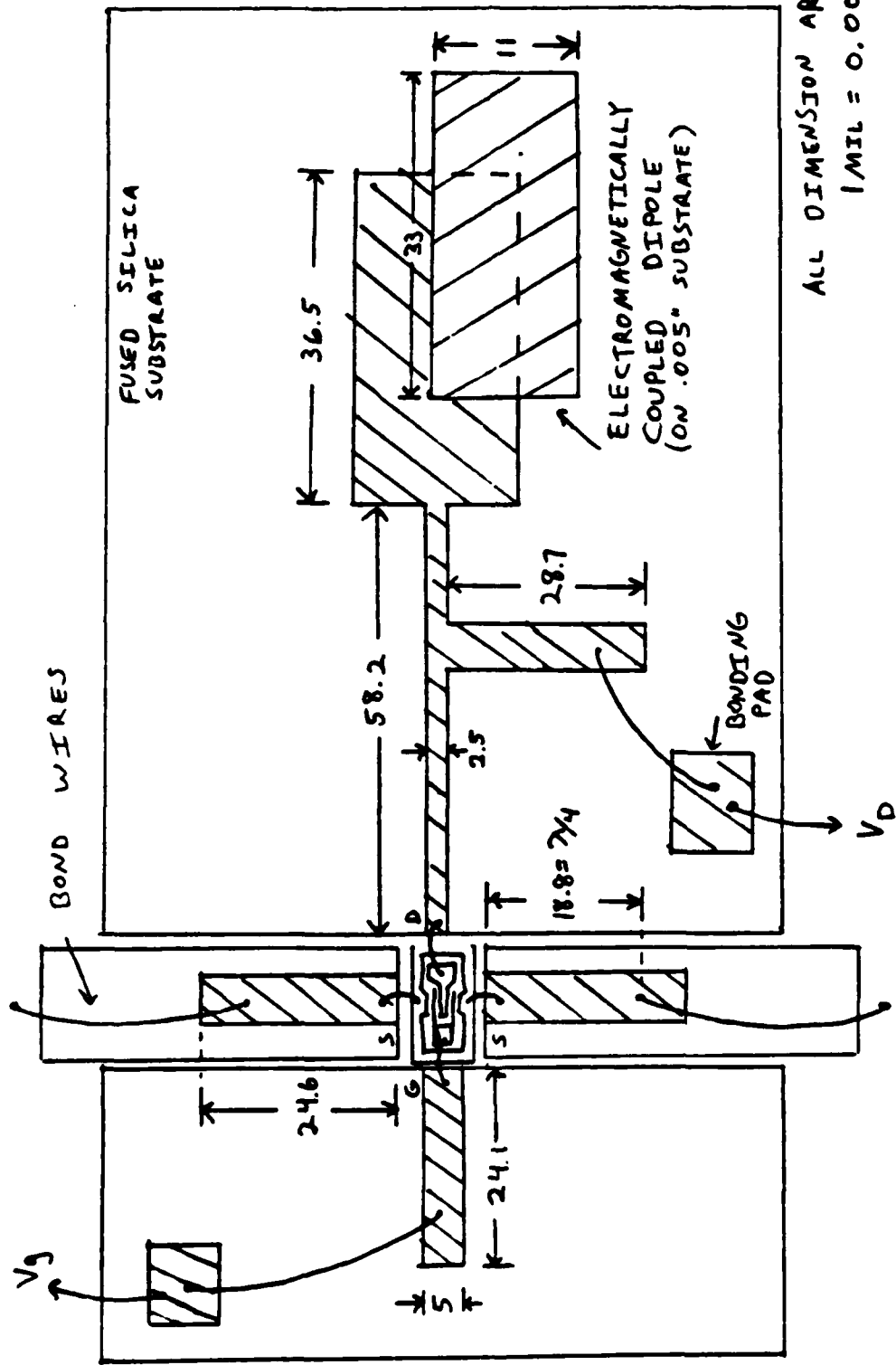


Figure 14. Oscillator Circuit Realization

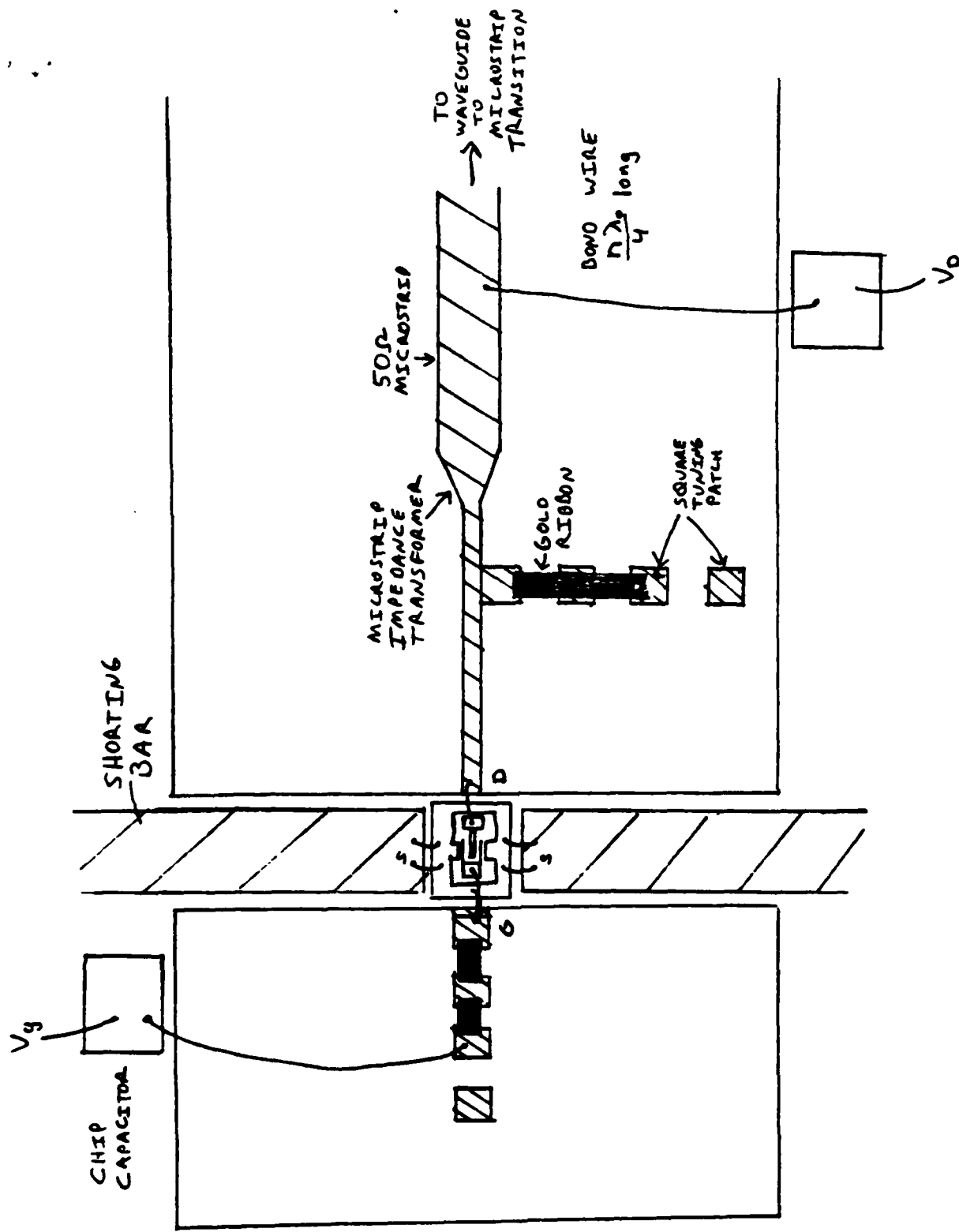


Figure 15. Improved Oscillator Design

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